IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

plication No.: 10/071,061

Applicant: Jui-Lin Hung

02/07/2002 Filed:

For: "Fuse Structure Used in An Integrated Circuit Device"

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Date: September 18, 2003 Examiner George C. Eckert II Group Art Unit 2815 Our Ref: 619518-8 🔆

Sir:

This paper responds to the Office Action dated June 19, 2003. Please amend the above-identified application as described below. All remarks herein are made without prejudice.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks begin on page 9 of this paper.